B (binary): Full points awarded if criteria met, no points otherwise.				
C (criteria): Grade according to the stated criteria for the requirements for each object				
S (split): points are split evenly across all artifacts				
	Possible	Point	Points	
	Points	Breakdown	Awarded	Notes
Testing				
AUTOMATED: To check testing, copy the group's code from their portfolio into a clean copy of the simulator				
framework and make sure the code will compile.	0	N/A	0	No points allocated for a compiling simulator.
Is the Unit Test Log complete and up to date (all controller modules listed, all tests passing, input and output				
files properly linked.	5	В		
AUTOMATED: Execute the unit tests using the simulator assembled in the design portfolio grading. (Note that				
this step requires a valid unit tests txt summary file). All tests must pass (0 failed assertions), and all tests listed				
in the unit test log must be listed in the unit tests.txt file. If the simulator will not compile, award no credit.	5	В		
Is the Integration Test Log complete and up to date? "Complete" means all sequence diagrams are tested (up				
to a total of 20) and include all the original sequence diagrams (1A, 1B, 1C, 2A, 2B, 3A, 4A, 5A, 5B, 6, 7A, 7B,				
7C, 8A, 9A). "Up to date" means all tests passing, input and output files properly linked.	5	В		
AUTOMATED: Execute the integration tests using the simulator assembled in the design portfolio grading.				
(Note that this step requires a valid integration tests.txt summary file). All tests must pass (0 failed assertions).				
and all tests listed in the integration test log must be listed in the integration tests bxt file. If the simulator will not				
compile, award no credit.	5	В		
Is the Acceptance Test Log complete and up to date? proj7acceptance1.pass, proj7acceptance2.pass, and				
proj7acceptance3.pass and proj8group#acceptance1.pass must all be listed. Each entry must be complete (all				
fields filled out and input and output files properly linked). Any test that does not pass must be documented to				
describe the problem that causes the test to fail.	5	В		
Does the group's own acceptance test meet the criteria listed in the project?	10	В		
AUTOMATED: Execute proj7acceptance1.pass, proj7acceptance2.pass, and proj7acceptance3.pass with an				
arbitrary random seed using the simulator assembled in the design portfolio grading. The test must deliver all				
passengers.	5	В		
Runtime Monitoring				
Are the runtime monitors for RT6 and RT7 present in the RuntimeRequirementsMonitor.java, and do they				
compile?	10	В		
Is the validation html up to date?	5	В		

Group#: Grader:

Project 8 Grade Sheet

Totals
Late Penalty
Enter the percentage of total score (per late policy)
Final Score
This is your actual grade

Is the Integration Test Log complete and up to date? "Complete" means all sequence diagrams are tested (up				
to a total of 20) and include all the original sequence diagrams (1A, 1B, 1C, 2A, 2B, 3A, 4A, 5A, 5B, 6, 7A, 7B, 7C, 8A, 9A). "Up to date" means all tests passing, input and output files properly linked.	_	В		
AUTOMATED: Execute the integration tests using the simulator assembled in the design portfolio grading.	5	В		
(Note that this step requires a valid integration_tests.txt summary file). All tests must pass (0 failed assertions),				
and all tests listed in the integration test log must be listed in the integration tests.txt file. If the simulator will not				
compile, award no credit.	5	В		
Is the Acceptance Test Log complete and up to date? proj7acceptance1.pass, proj7acceptance2.pass, and				
proj7acceptance3.pass and proj8group#acceptance1.pass must all be listed. Each entry must be complete (all				
fields filled out and input and output files properly linked). Any test that does not pass must be documented to				
describe the problem that causes the test to fail.	5	В		
Does the group's own acceptance test meet the criteria listed in the project?	10			
AUTOMATED: Execute proj7acceptance1.pass, proj7acceptance2.pass, and proj7acceptance3.pass with an				
arbitrary random seed using the simulator assembled in the design portfolio grading. The test must deliver all				
passengers.	5	В		
Runtime Monitoring				
Are the runtime monitors for RT6 and RT7 present in the RuntimeRequirementsMonitor.java, and do they				
compile?	10			
Is the validation.html up to date?	5	В		
Execute proj7acceptance1.pass, and see if the monitor output matches the output described in verification.html	5	В		
Sequence Diagrams				
Look at the table of contents in the Scenarios and Sequence Diagrams document. Is every requirement (R-T6				
through R-T10) listed by at least one sequence diagram?	10	В		
Choose four sequence diagrams at random from the ones that are traced to a high level requirement in the				
table of contents. Check that part of it actually pertains to the traced requirement. For example, if a sequence				
diagram is traced to R-T5, then it must show the elevator being commanded to FAST. If a sequence diagram is				
traced to R-T8.1, it must show the lanterns being turned on.	10	В		
Choose four sequence diagrams at random from the ones that are traced to a high level requirement in the				
table of contents. Check that the sequence diagrams are well-formed, e.g. mMessage[f,b] notation, blue				
arrows for physical values, black arrows for network messages, each arc has a unique number. Note that arc		_		
numbering does NOT have to be contiguous. Time Triggered Requirements	10	В		
	· .	_		
Does each requirement use precise words (shall and should an no other verb)?	4	В		"If mMacaga VII h I a caught a " in accordable but "If mMacaga VII h I is received as" in
Does each requirement use precise words (shall and should an no other verb)?	4			"If mMessageX[f,b] s equal to" is acceptable, but "If mMessageX[f,b] is received as" is
	4	В		"if mMessageX[f,b] s equal to" is acceptable, but "if mMessageX[f,b] is received as" is unacceptable.
Does each requirement use precise words (shall and should an no other verb)?	4			unacceptable.
Does each requirement use precise words (shall and should an no other verb)?	4			
Does each requirement use precise words (shall and should an no other verb)?	4			unacceptable. For multi-part requirements, "If X, then Y shall be set to True and Z shall be set to False", the each
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Does each requirement use precise words (shall and should an no other verb)? Does each requirement contain a reference to state variables and not to message delivery or other events? Does every verb in each requirement have a unique number? Does the sequence-diagrams-to-requirements table contain all the updated requirements and sequence	4	В		unacceptable. For multi-part requirements, "If X, then Y shall be set to True and Z shall be set to False", the each "shall" is a verb, so they should be sub-numbered (a) and (b) — see the project writeup for more details. If a requirement says "IX, then Y shall be set to True and Z to False", this is still a two-part
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