



Annual IEEE/ACM International Symposium on Microarchitecture

MICRO Test of Time Award 2017

Call for Nominations (Deadline: July 15, 2017)

The MICRO Test of Time (ToT) Award Committee is soliciting nominations for the fourth MICRO ToT Award to be given at the International Symposium on Microarchitecture in October 2017, to be held in Boston, MA. This recognizes the most influential papers published in past MICRO conferences that have had significant impact in the field.

The award will recognize an influential MICRO paper whose influence is still felt 18-22 years after its initial publication. In other words, the award will be given to at most one paper that was published at MICRO conferences in any of the years N-22, N-21, N-20, N-19, or N-18. This year, N = 2017, so only papers published at MICRO conferences held in 1995, 1996, 1997, 1998, or 1999 are eligible. An eligible paper that has received at least 100 citations (according to Google Scholar) is automatically nominated, but explicit nominations of such papers are still encouraged.

We invite anyone to submit a nomination by the deadline of **July 15, 2017**. Please submit your nomination via email to micro-tot-award-nominations@googlegroups.com. The following describes the nomination criteria, how to submit a nomination, and the selection process.

- **Which papers are eligible for the 2017 MICRO ToT (Test of Time) Award?**
Only papers published at MICRO conferences that happened between 1995–1999 (1995 and 1999 inclusive). The list of eligible papers is at <http://www.microarch.org/Micro-ToT-Award/eligible2017.html>
- **Who can nominate a paper?**
Anyone can nominate a paper except for the author or co-author of the nominated paper.
- **Is there a limit on the number of papers one person can nominate?**
Yes. One can nominate **up to 5 papers** from all eligible papers. These five papers can be published in any eligible year.

- **When is the last day to submit a nomination?**
Nominations must be received by **July 15, 2017**.
- **How should a nomination be submitted?**
Nominations must be submitted via email to micro-tot-award-nominations@googlegroups.com.
- **What should be included in the nomination email?**
 1. The title, the author list, and publication year of the nominated paper
 2. A 100-word (maximum) nomination statement, describing why the paper deserves the Test of Time Award
 3. The name, title, affiliation of the nominator, and if appropriate, the relationship of the nominator to the authors

Only one paper can be nominated in a single email.
- **What is the selection process?**
The MICRO ToT Award committee will evaluate all submitted nominations after July 16, 2017 to select at most one paper for the 2017 MICRO ToT (Test of Time) Award. A strict conflict of interest policy will be followed. If a ToT Award Committee Member has a conflict of interest with a paper that is nominated, the member will recuse himself/herself from the discussion process and a substitute will be placed in.
- **Who are the current MICRO ToT Award committee members?**
Rich Belgard
Pradip Bose
Bill Mangione-Smith
Onur Mutlu, *chair*
Andre Seznec
Uri Weiser
- **When will the award be announced and given?**
The award will be presented to the authors of the selected paper at the Awards Ceremony during the International Symposium on Microarchitecture in October 2017, to be held in Boston, MA. Awardees are not expected to make a presentation.
- **What does the award consist of?**
An award certificate and peer recognition. The authors of the selected paper are expected to be invited to write a retrospective of the paper in the IEEE Micro magazine.
- **What papers were previous winners of the award?**
A list of all previous winners of the MICRO ToT Award is available online at <http://www.microarch.org/Micro-ToT-Award/index.html>.