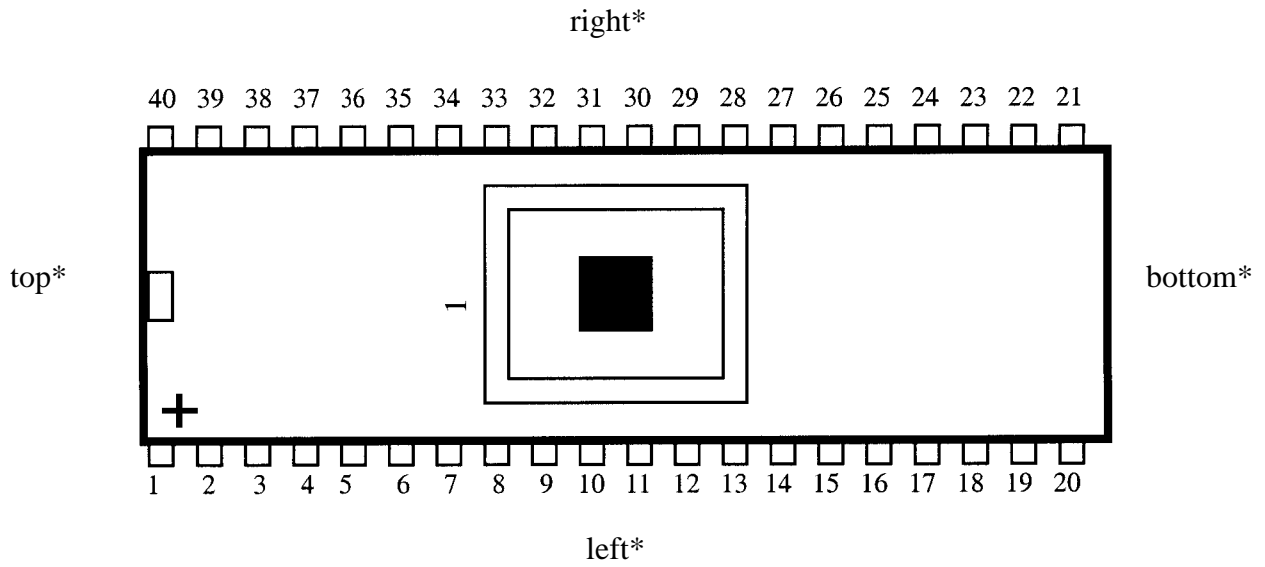


# MIT Resonator Chip: Test Structure Resonator Bond Pattern

This chip contains 6 degree-of-freedom resonators designed by Salil Desai, currently at MIT. The chip also contains a test structure resonators designed at Carnegie Mellon.



|    |  |    |  |    |              |    |  |    |               |
|----|--|----|--|----|--------------|----|--|----|---------------|
| P# |  | P# |  | P# |              | P# |  | P# |               |
| 1  |  | 9  |  | 17 |              | 25 |  | 33 |               |
| 2  |  | 10 |  | 18 |              | 26 |  | 34 |               |
| 3  |  | 11 |  | 19 | m3Field      | 27 |  | 35 |               |
| 4  |  | 12 |  | 20 | substrate    | 28 |  | 36 |               |
| 5  |  | 13 |  | 21 | ff_plate     | 29 |  | 37 |               |
| 6  |  | 14 |  | 22 | cl_leftcomb  | 30 |  | 38 | cl_plate      |
| 7  |  | 15 |  | 23 | cl_rightcomb | 31 |  | 39 | ff_bottomcomb |
| 8  |  | 16 |  | 24 |              | 32 |  | 40 | ff_topcomb    |

ff: folded flexure

cl: crab leg

\* indicates orientation of the die inside the package