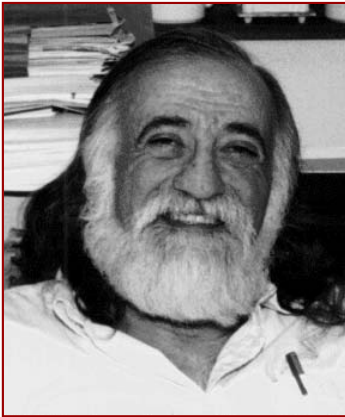


THURSDAY
APRIL 29, 2004

Scaife Hall Auditorium
Room 125

4:00 PM
Refreshments—3:30 PM



Yale Patt

UNIVERSITY OF TEXAS—AUSTIN

Yale Patt is Professor of Electrical and Computer Engineering and the Ernest Cockrell, Jr. Centennial Chair in Engineering at UT Austin. He supervises the research of 12 PhD students in microarchitecture, loves teaching freshmen, seniors and graduate students, and consults extensively in the microprocessor industry. He has the appropriate degrees from reputable universities and more than enough awards for his research and teaching.


Further detail at
<http://www.ece.utexas.edu/~patt>.

For more information:
<http://www.ece.cmu.edu/seminar>

James C. Hoe, ECE Seminar Host
jhoe@ece.cmu.edu

THE MICROPROCESSOR TEN YEARS FROM NOW: WHAT ARE THE CHALLENGES, HOW DO WE MEET THEM?

Notwithstanding the continual prognosis of the demise of Moore's Law, I expect continued exponential growth from the process technology people over the next decade. How do we harness that into "Moore's Law" growth in performance? The challenges seem to be power consumption, on-chip/off-chip exacerbation, complexity. In this talk, I hope to look at some of these challenges and where we might find solutions.

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