

ERIC S. CHUNG

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- RESEARCH** FPGA-based simulation tools, multiprocessor architecture, reliability
- EDUCATION** 2004 – Present Carnegie Mellon University – Pittsburgh, PA
- **PhD candidate in Electrical and Computer Engineering**
 - Advised by Professor James C. Hoe at the Computer Architecture Lab at Carnegie Mellon
 - Relevant courses: Advanced Computer Architecture, Multiprocessor Computer Architecture, Advanced Operating Systems & Distributed Systems, Advanced Techniques in Microprocessors, Advanced Digital Integrated Circuits Design
- August 2001 – May 2004 University of California Berkeley – Berkeley, CA
- **Bachelor of Science in Electrical Engineering and Computer Science**
- FELLOWSHIPS AND AWARDS**
- Carnegie Mellon Laboratory for Computer Systems Fellowship
 - Honors Graduation at UC Berkeley
 - Eta Kappa Nu: Berkeley Mu Chapter
 - Tau Beta Pi Invitation
 - Golden Key International Honor Society
 - National Collegiate Inventors & Innovators Alliance Advanced E-Team Award (2004)
 - World Journal Scholarship (2001-2004)
 - Silicon Valley Scholars Award (2000)
- RESEARCH EXPERIENCE**
- **Computer Architecture Lab at Carnegie Mellon** – Graduate Assistant, Pittsburgh, PA (2004 – now)
Principal developer for the PROTOFLEX hybrid FPGA platform (<http://www.ece.cmu.edu/~simflex>). Recently demonstrated the first FPGA-based emulator that can model an UltraSPARC III workstation running commercial Solaris. Designing virtualization techniques for efficiently multiplexing the execution of thousands of processors onto a small number of high-throughput emulation engines. Also using PROTOFLEX to study reliability for large-scale systems. (<http://www.ece.cmu.edu/~truss>)
 - **Research Accelerator for Multiple Processors** – Contributor (2005 – now)
RAMP is a multi-university collaborative effort to develop FPGA-based emulators of large-scale multiprocessor systems (<http://ramp.eecs.berkeley.edu>). Contributing effort to build components for a large-scale shared-memory multiprocessor emulator.
 - **Berkeley Innovation** - Co-founder and Project Team Leader, Berkeley, California, (2003 - 2005)
Co-founder of multidisciplinary industrial design group (<http://innovation.berkeley.edu>). Petitioned for and awarded the NCIIA Advanced E-Team Award for \$20,000. Received funding and support from Nokia, IDEO, and Microsoft. Group leader for the inexpensive wireless Shuttle Tracking System. Prototypes were delivered in 2005 to the local campus bus agency.
 - **Group for User Interface Research** - Research Assistant, Berkeley, California, (2002 – 2004)
Worked with Professor James Landay and Professor Jason Hong to identify and catalog design patterns in Ubiquitous Computing (continued at the Intel Research Laboratory at Seattle).
- TEACHING EXPERIENCE**
- **Fundamentals of Computer Engineering** at CMU with Prof. James Hoe (2006, TA rating: 4.5/5.0)
 - **Advanced Computer Architecture** at CMU with Prof. James Hoe (2005, TA rating: 4.7/5.0)
 - **Design Techniques for Digital Systems** at UCB with Prof. Randy Katz (2004, TA rating: 4.1/5.0)
 - **Intro to Digital Electronics** at UCB with Prof. Andrew Neureuther (2003, TA rating: 4.8/5.0)